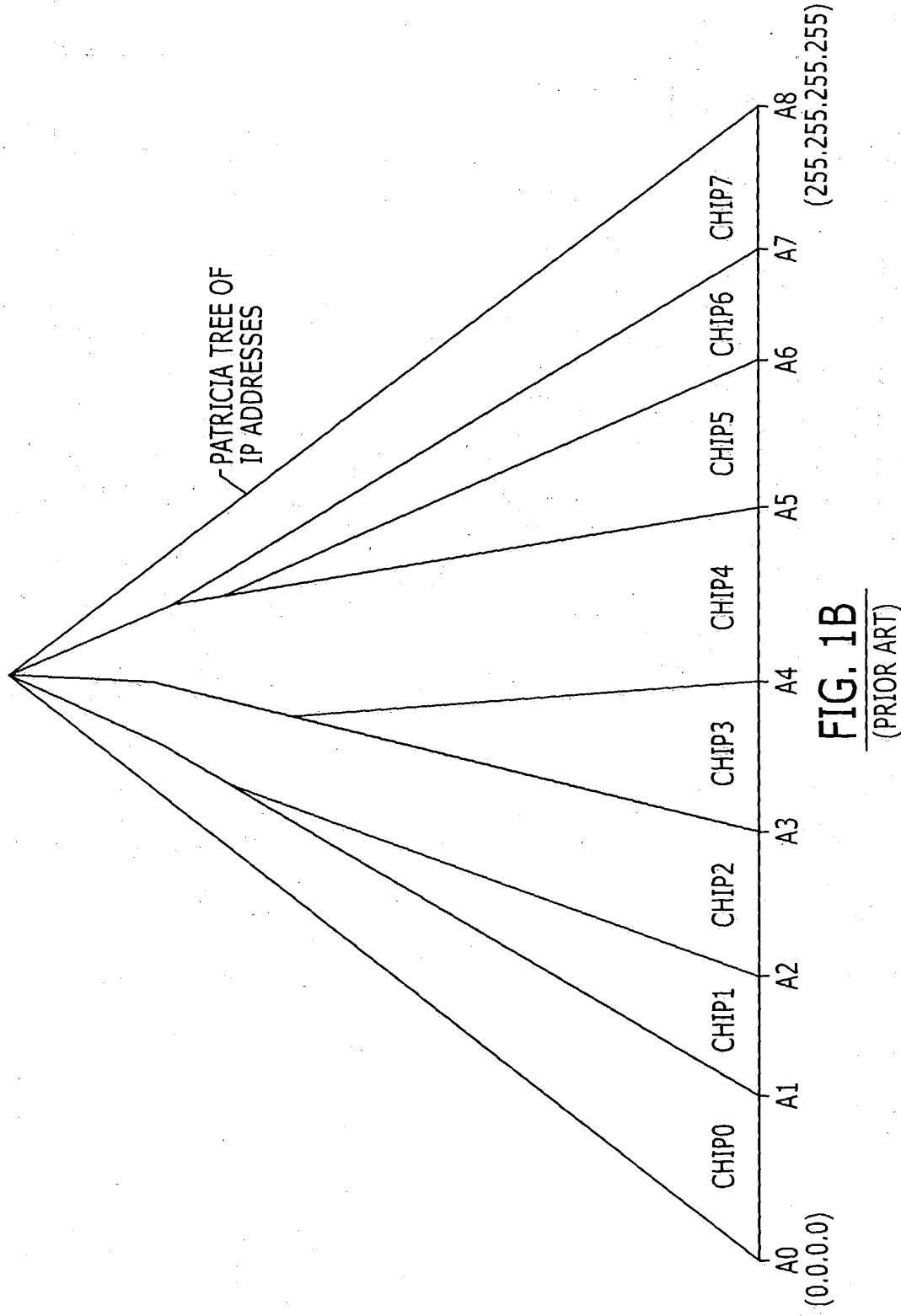


**FIG. 1A**  
(PRIOR ART)



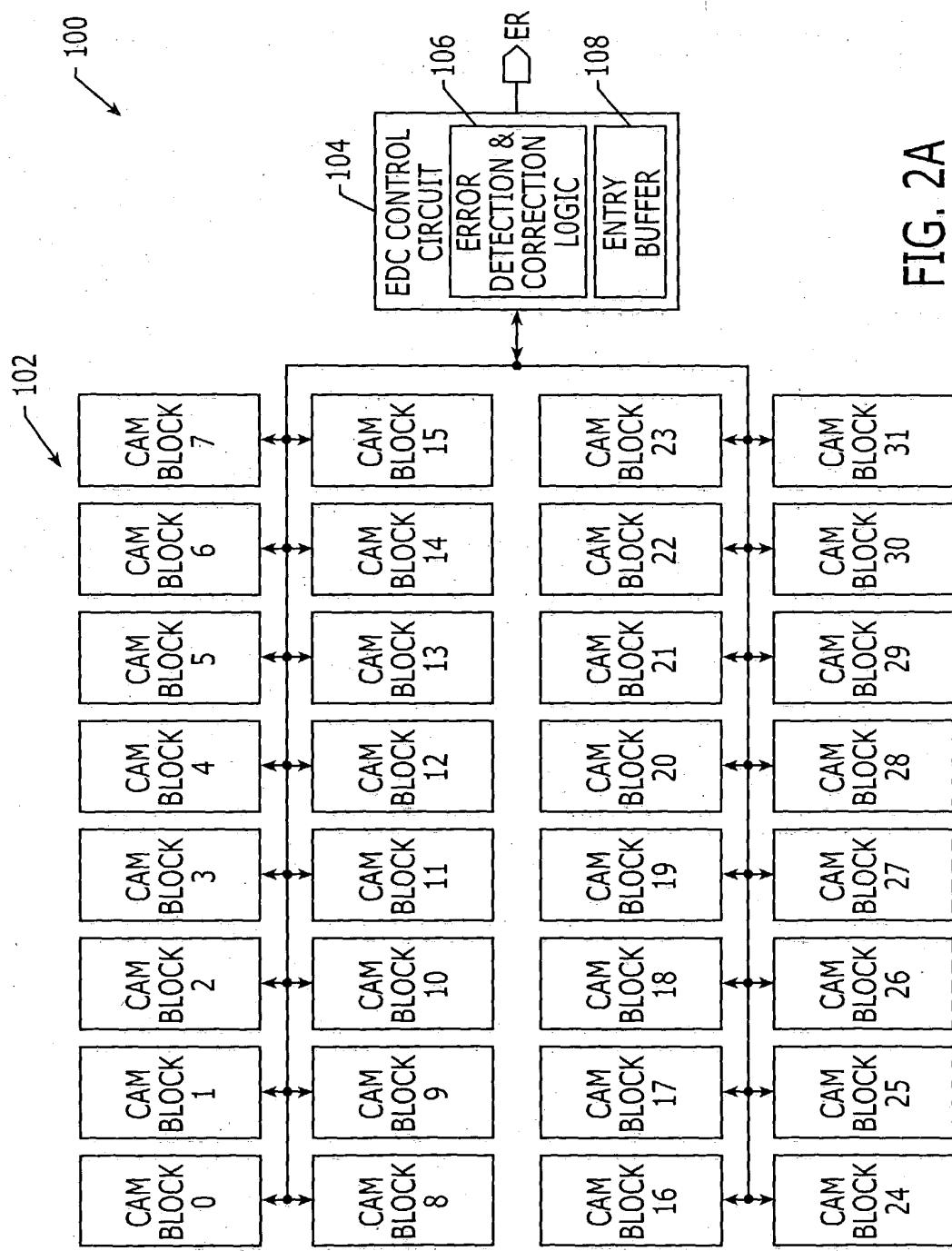
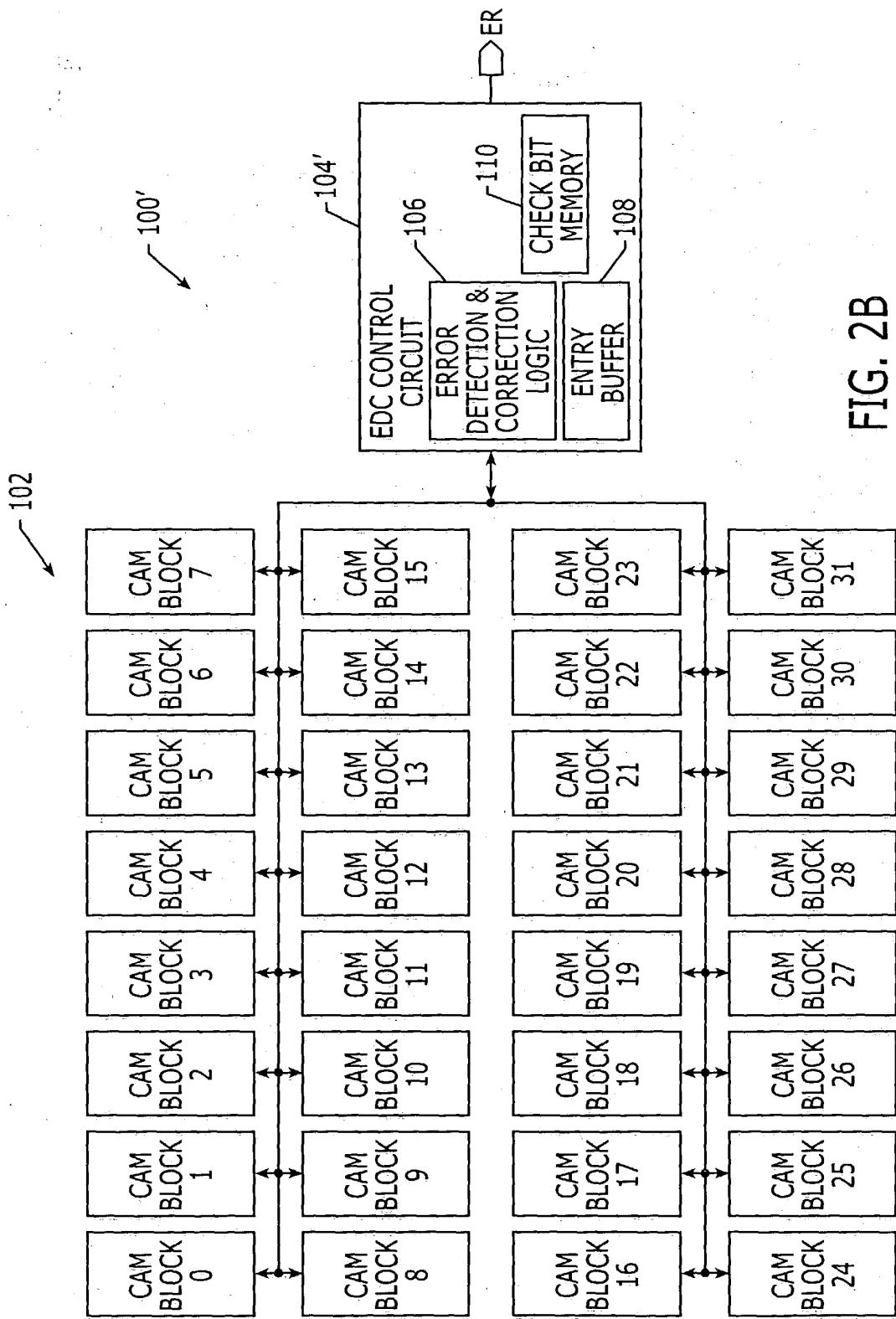


FIG. 2A



**FIG. 2B**

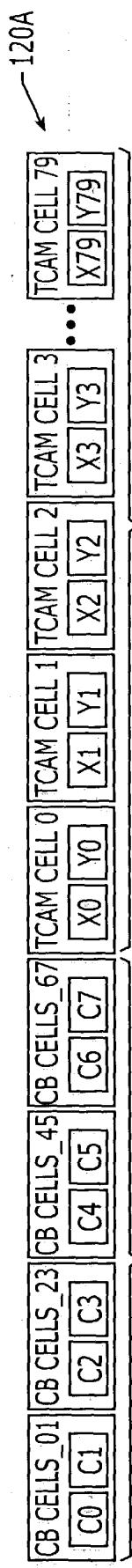


FIG. 3A

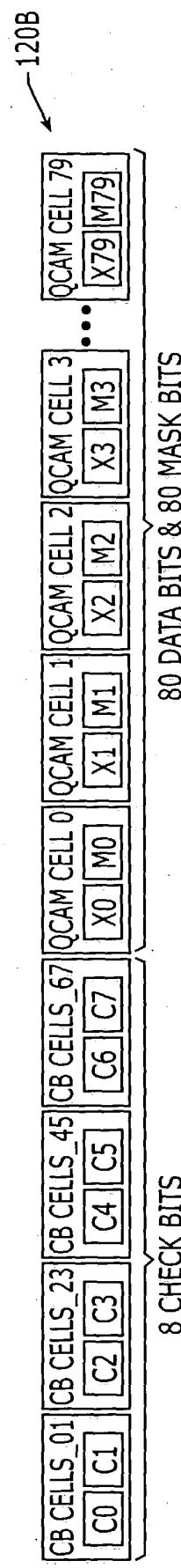


FIG. 3B

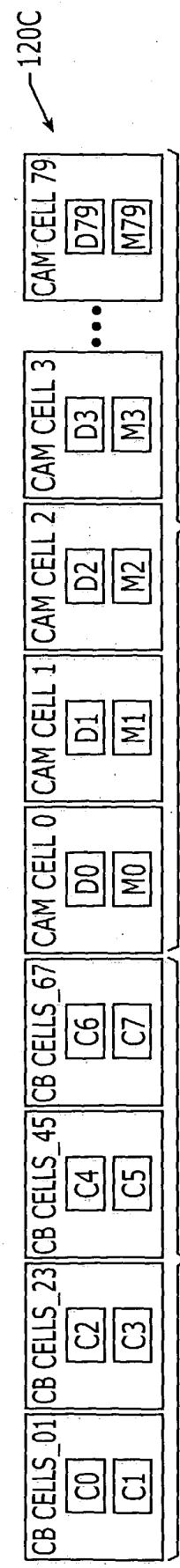
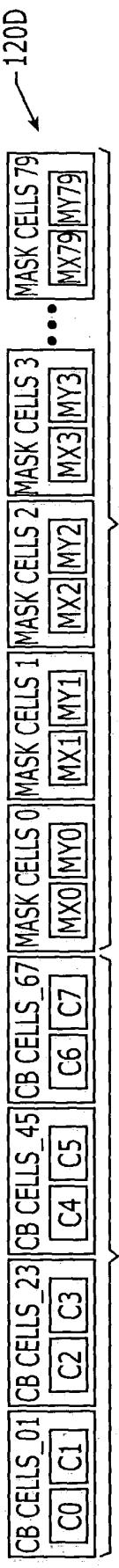
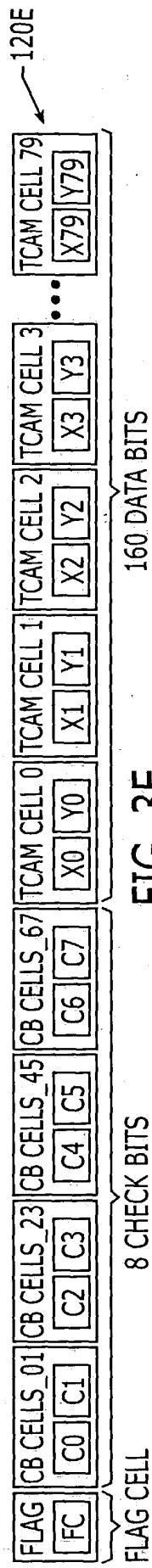


FIG. 3C



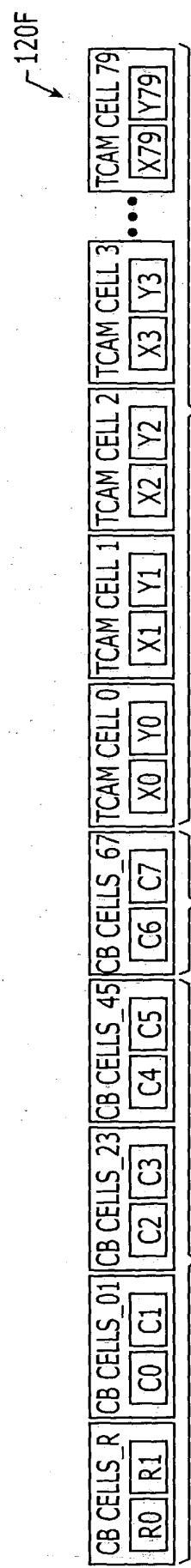
2 80-BIT GLOBAL MASKS

FIG. 3D



160 DATA BITS

FIG. 3E



160 DATA BITS

FIG. 3F

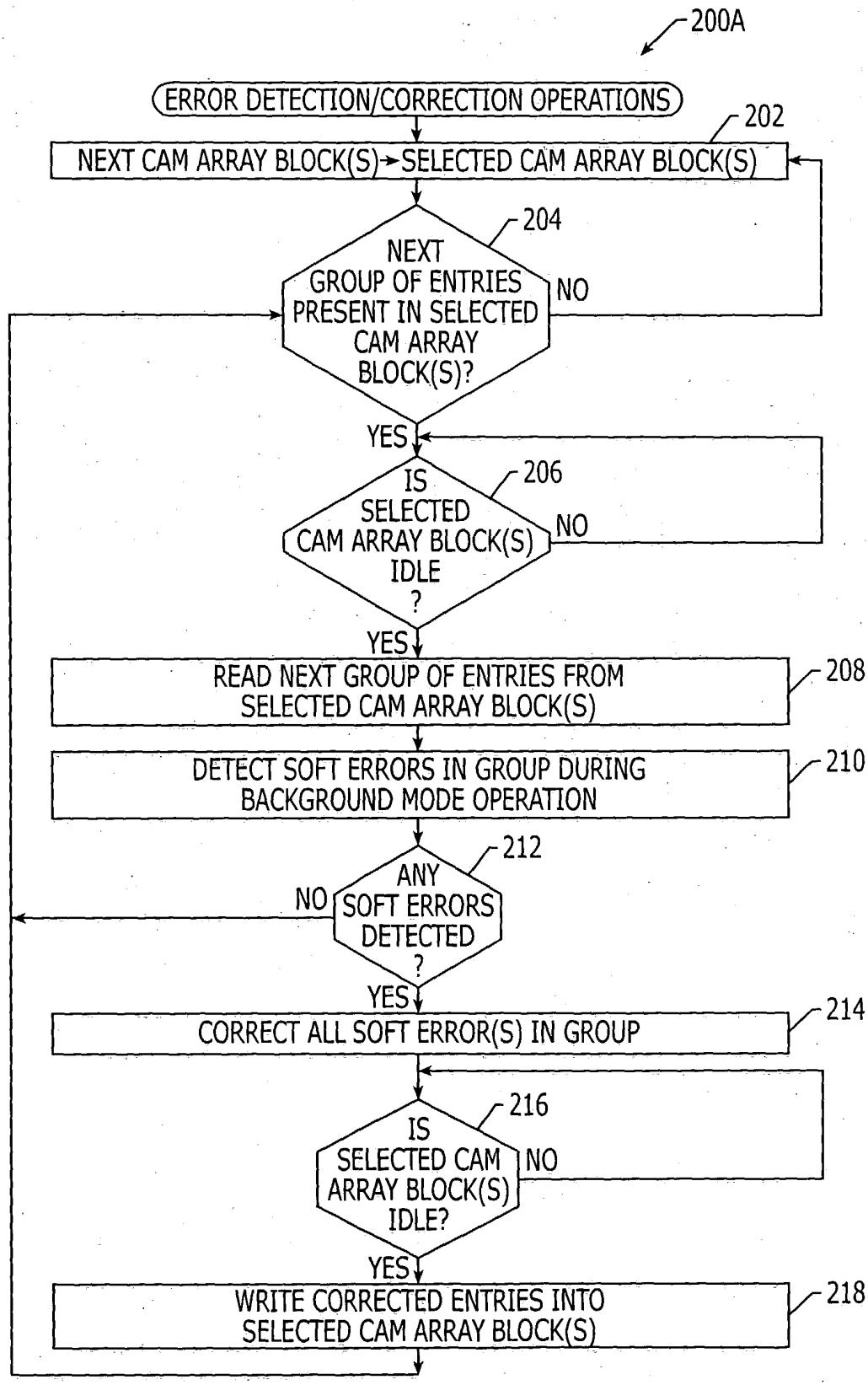


FIG. 4A

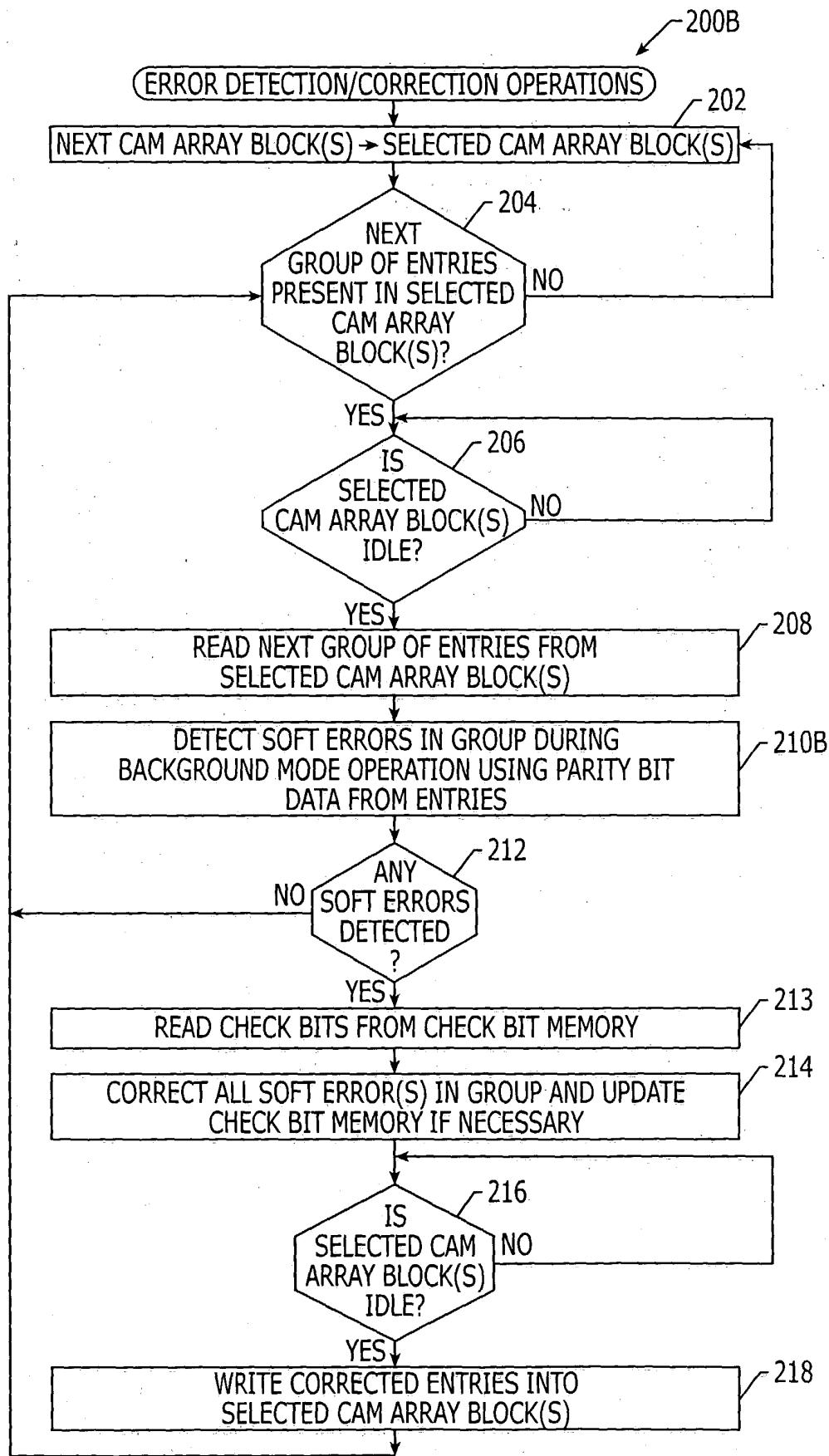
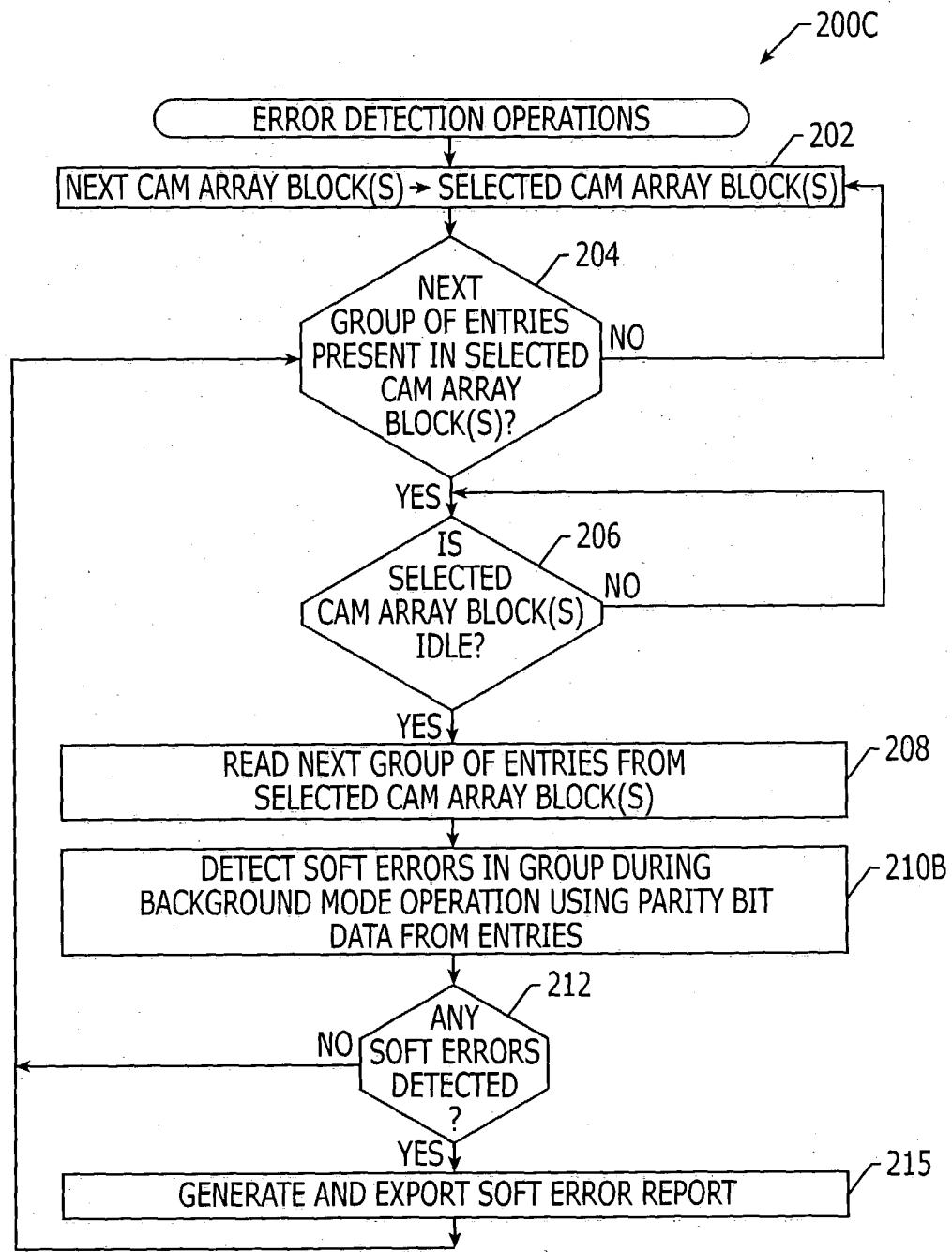
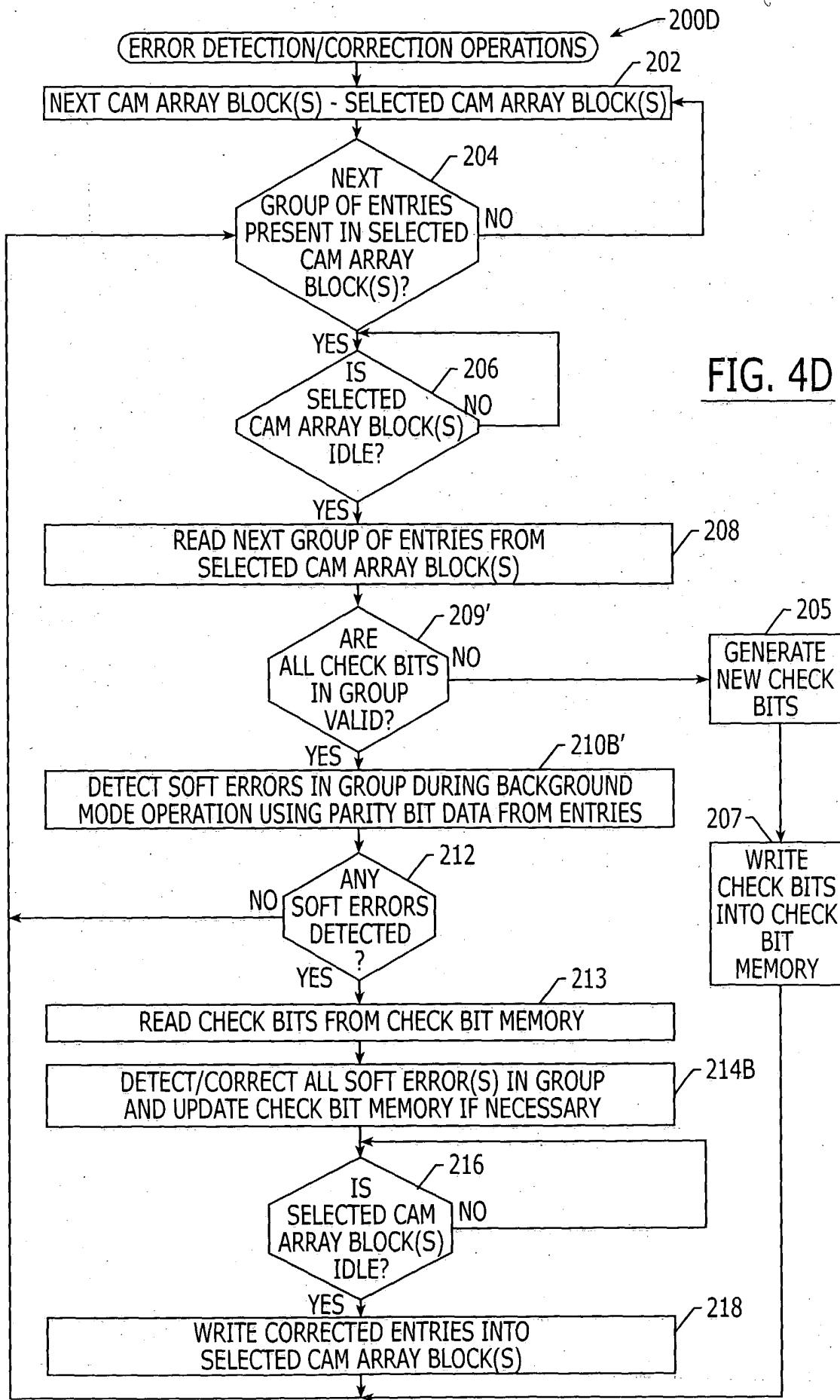
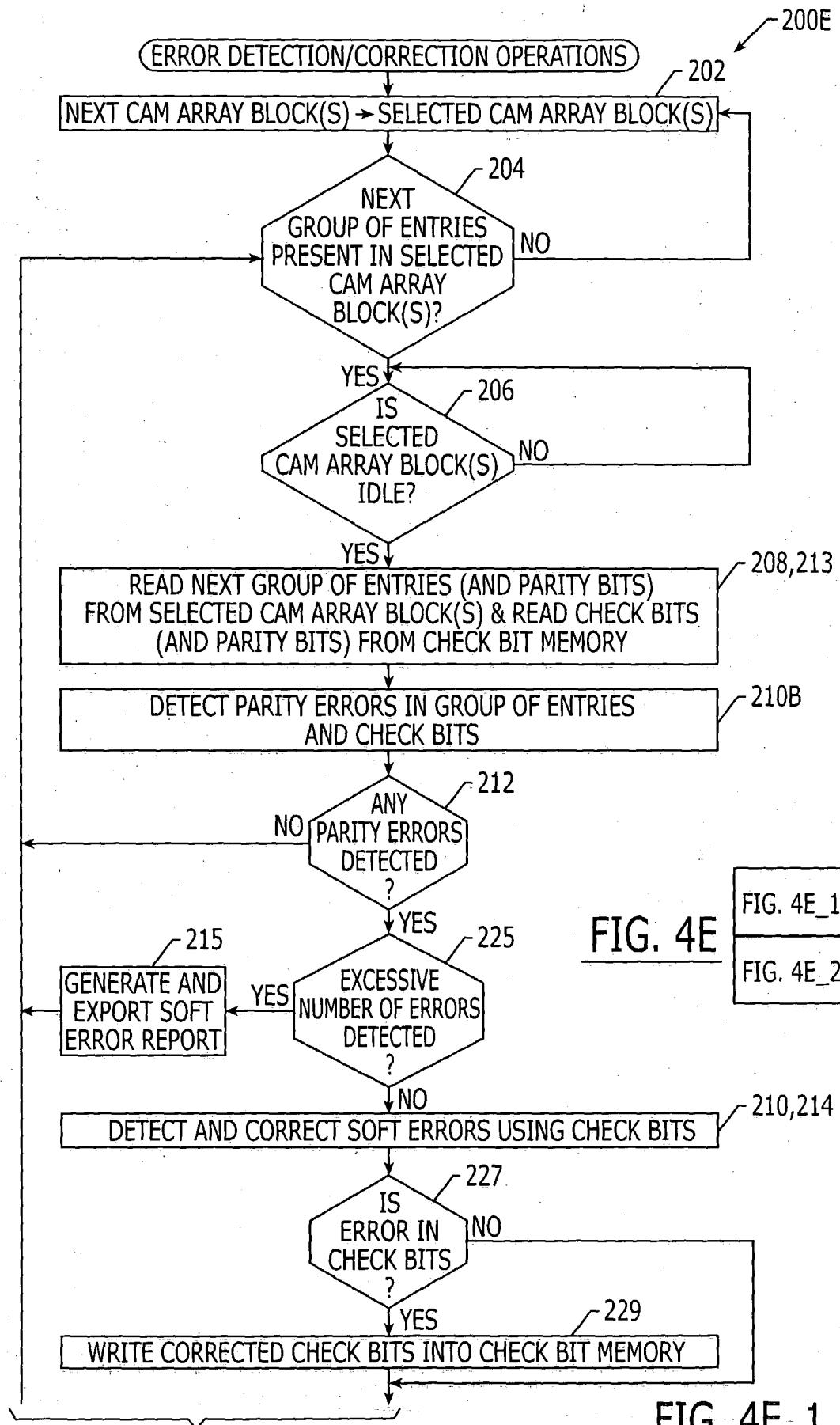


FIG. 4B







FROM FIG. 4E\_1

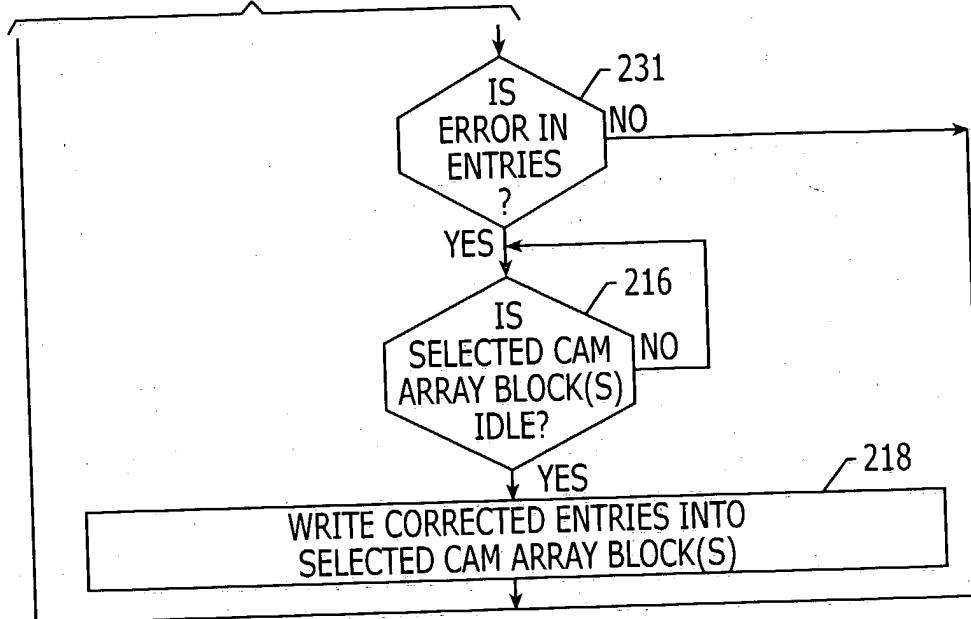
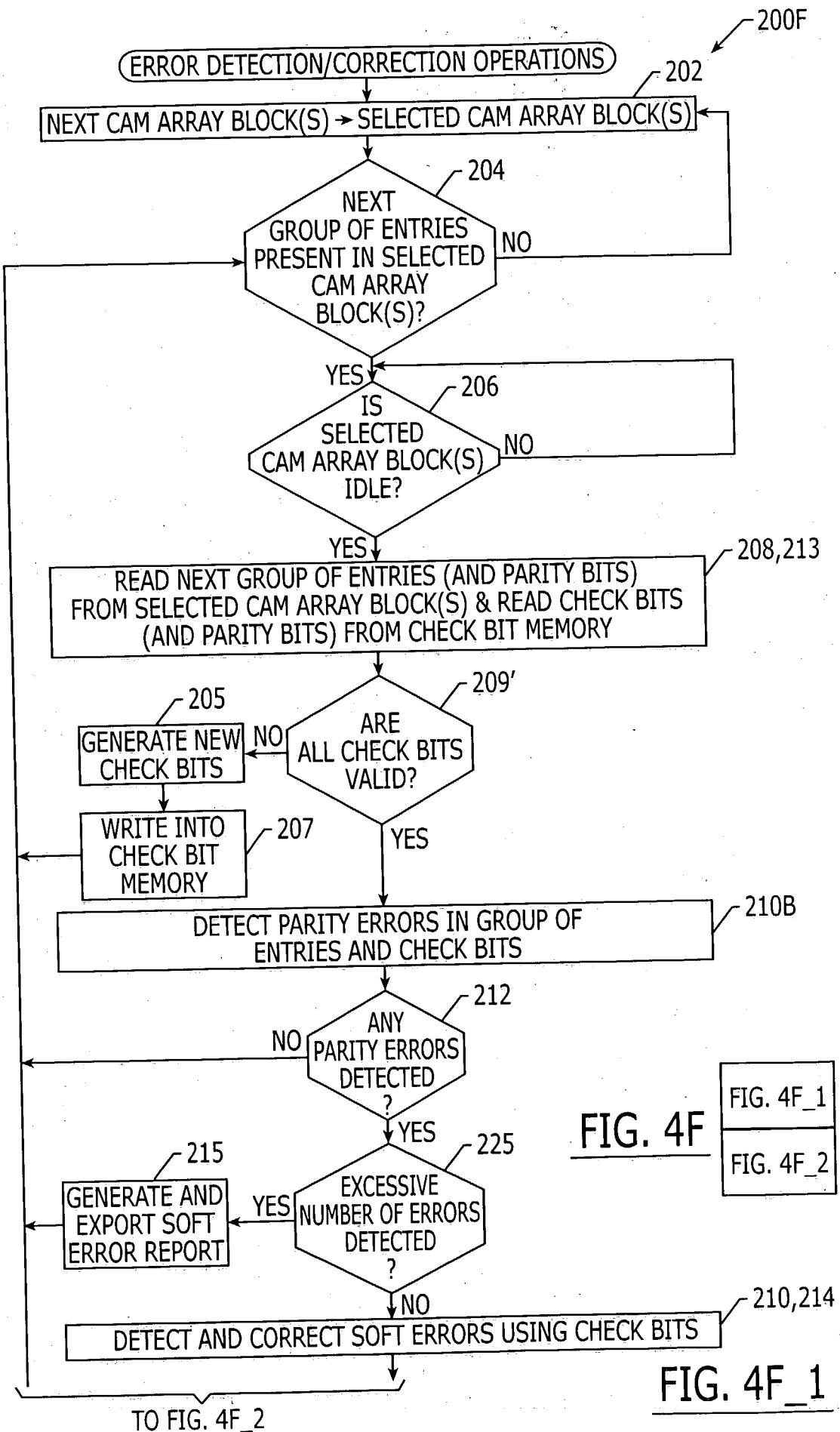


FIG. 4E\_2



FROM FIG. 4F\_1

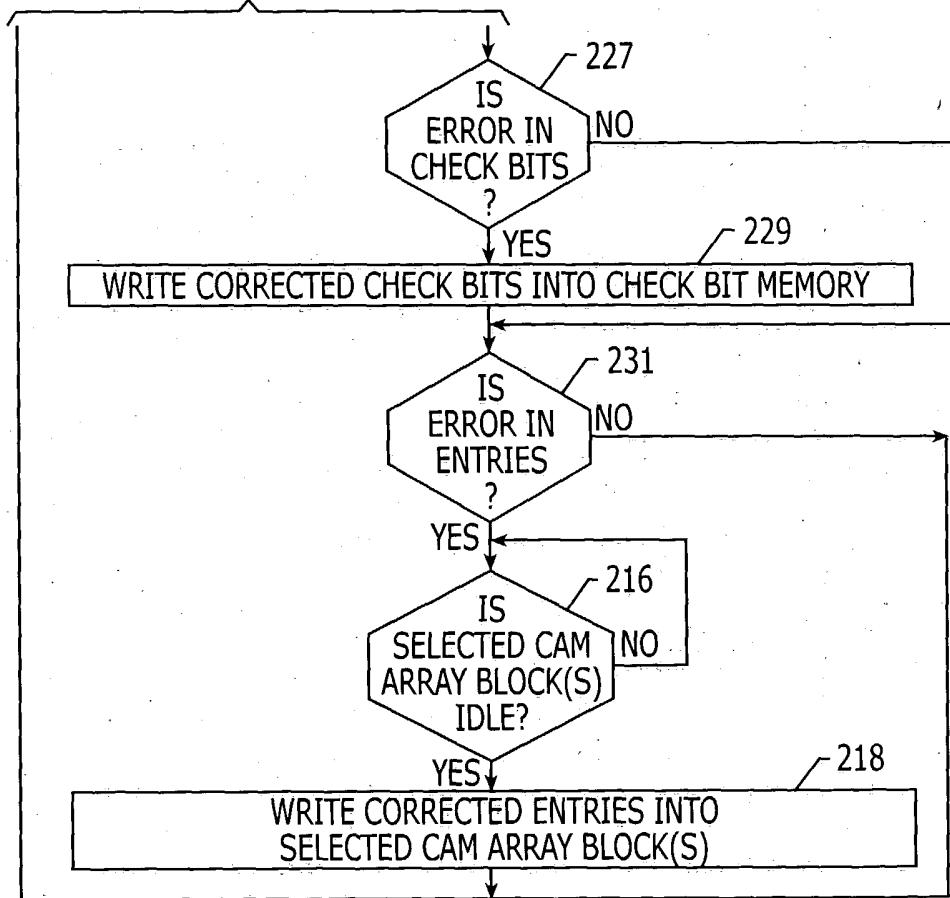


FIG. 4F\_2

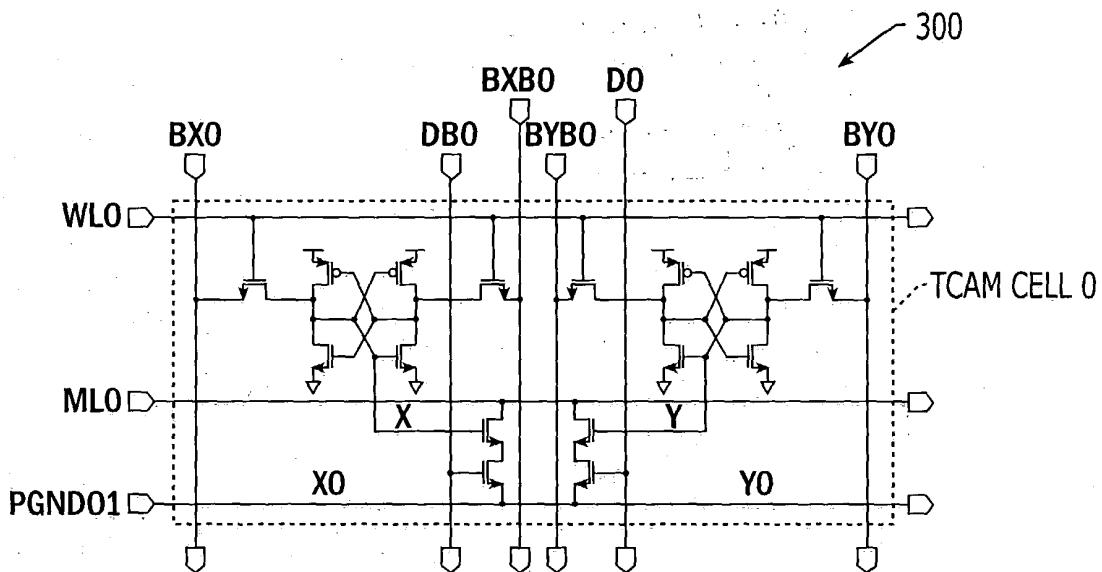


FIG. 5A

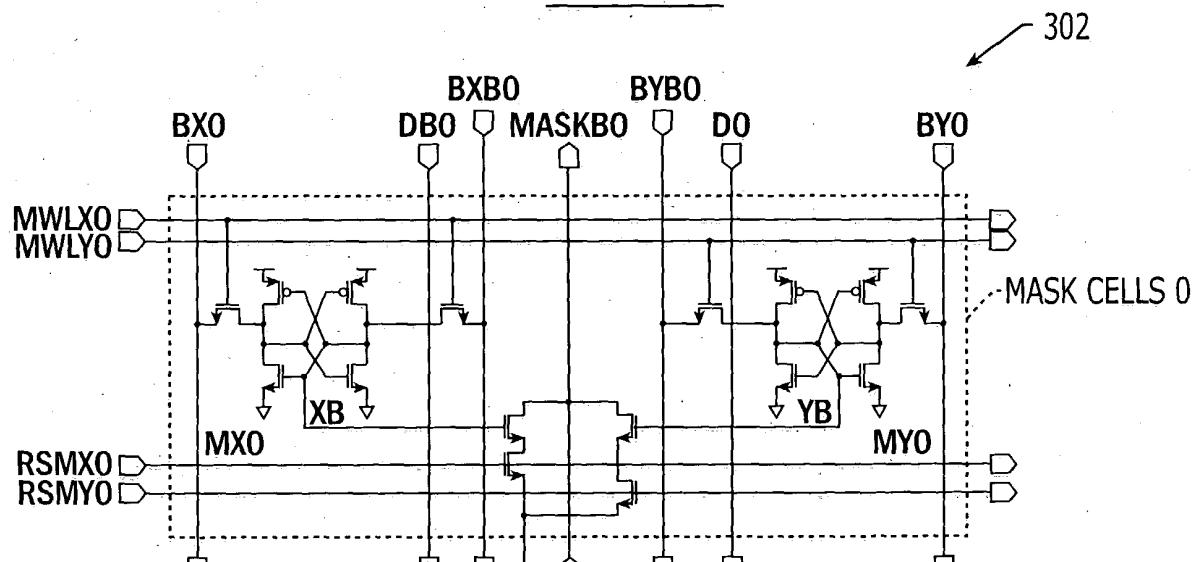


FIG. 5B

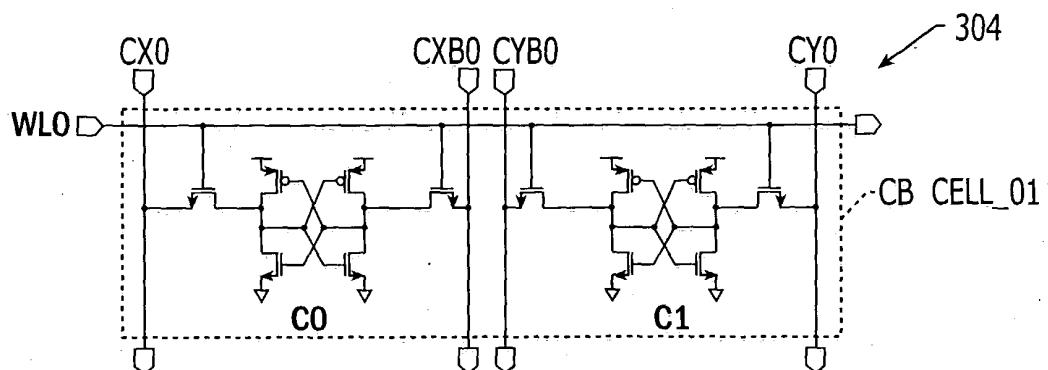


FIG. 5C

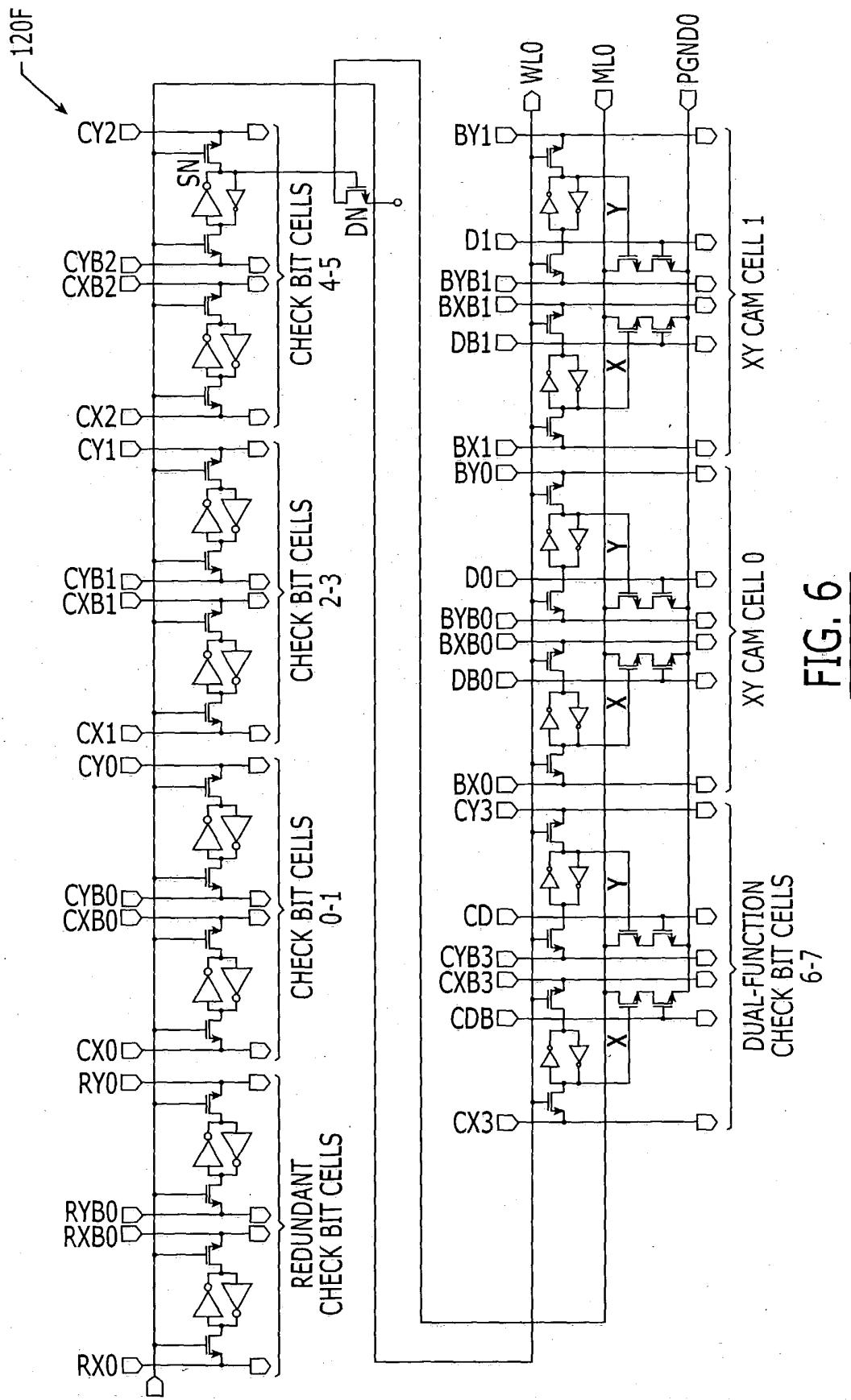


FIG. 6